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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10091405	FILING DATE 03/07/2002	CLASS 349/463	SUBCLASS 147	GAU 2838	EXAMINER SHAPIRO
<b>**APPLICANTS:</b> Endo Kazuya; Miyamoto Naoki; Mizuno Toshio; Nakaji Takayuki; Uchida Takafoshi; Ookado Kazuo; Yokota Yoshikazu; 363 147 2838					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 2001-110813 04/10/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO H-1030	
<b>TITLE :</b> Semiconductor integrated circuit with voltage generation circuit, liquid crystal display controller and mobile electric equipment					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-435L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>Assistant Examiner</b>	<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>Primary Examiner</b>	<b>DRAWING</b>	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	<b>Application Examiner</b>	
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